ASSISTANT COMMISSIONER FOR Washington, D.C. 20231

Transmitted herewith for filing under 37 C.F.R. §1.53(b) is the patent application of

leventor(s): Tsuyoshi TOMITA

SHIGNAL PROCESSING CIRCUIT FOR PREVENTING PSEUDO LOCK OF TIMING RECOVERY PLI

Specification and Claims (26 pages)

5 sheets of drawings

Newly executed Declaration and Power of Attorney

XX Return Receipt Postcard

XX An assignment of the invention to FUJITSU LIMITED with accompanying PTO-1595 Form

<u>XX</u> A certified copy of Japanese Patent Application No. 10-265224 filed: September 18, 1998

A verified stmt. to establish small entity status under 37 C.F.R. §1.9 and 37 C.F.R. §1.27

XXA filing fee, calculated as shown below:

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FÖR:	No. Filed	No. Extra	RATE
BASIC FEE			
TOTAL CLAIMS	15 - 20 =	* 0	× 9=
INDEP CLAIMS	3 - 03 =	* 0	× 39 =
MULTIPLE D	+130 =		
## If the difference	TOTAL		

If the difference in Col. 1 is less than zero, enter "0" in

Small Entitud

Small Entity			
FEE			
\$380			

or

or

or

or

or

Other Than A Small Entity

RATE	FEE	
	\$760	
× 18 =	0	
× 78 =	0	
+260 =	0	
	\$760	

in the amount of \$800.00 to cover the filing fee and assignment recordation fee. In the event that the attached check is found to be insufficient, the Commissioner is hereby authorized to charge payment for any additional filing fees required under 37 CFR 1.16 associated with this communication or credit any over-payment to Deposit Account No. 14-1060.

Respectfully submitted,

NIKAIDO, MARMELSZEIN, MURRA

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